



P/3236-39

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Kenichi Koyanagi et al.

Date: June 6, 2005

Serial No.: 10/630,444

Group Art Unit: 2823

Filed: July 29, 2003

Examiner: William D. COLEMAN

For: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

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dated
8-11-05
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT/SUBMISSION

Sir:

This is a response to the Office Action mailed March 16, 2005 in the above-identified application. Reconsideration of the application is respectfully requested.

FEE CALCULATION

Any additional fee required has been calculated as follows:

 If checked, "Small Entity" status is claimed.

NO. CLAIMS AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	EXTRA PRESENT	RATE	ADDIT. FEE
TOTAL 37	MINUS 37	* = 0	X (\$25 SE or \$50)	\$0
INDEP. 3	MINUS 3	** = 0	X (\$100 SE or \$200)	\$0
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM			X (\$180 SE or \$360)	\$0

* not less than 20

** not less than 3

TOTAL \$0

If any additional payment is required, a check which includes the calculated fee of \$ _____
(OFGS Check No. _____) is attached.